System-on-Chip

Product Letter

SYSTEM-ON-CHIP LITE

Development Board

Description

System-on-Chip Lite (SoCLite) is NEC's new approach for low- to mid-volume SOC projects. With the SoCLite development board, customers obtain a complete prototyping system for fast and easy development and verification of their SoCLite implementations. The main components of the board are the SoCLite prototype chip and a large FPGA. The prototype chip contains the complete ARM subsystem, while the FPGA is intended for the user defined logic (UDL). An FPGA development environment from either Xilinx or Altera can be used to design the UDL part. The prototype chip and the FPGA make up a complete SoCLite device. All connections between the prototype chip and the FPGA are identical to the internal connections between the ARM subsystem and the UDL in the final SoCLite chip. All signals such as APB, interrupts, memory bus and JTAG interface are available at external connectors for test and evaluation purposes, as are the external UDL signals (133 pins).

Features

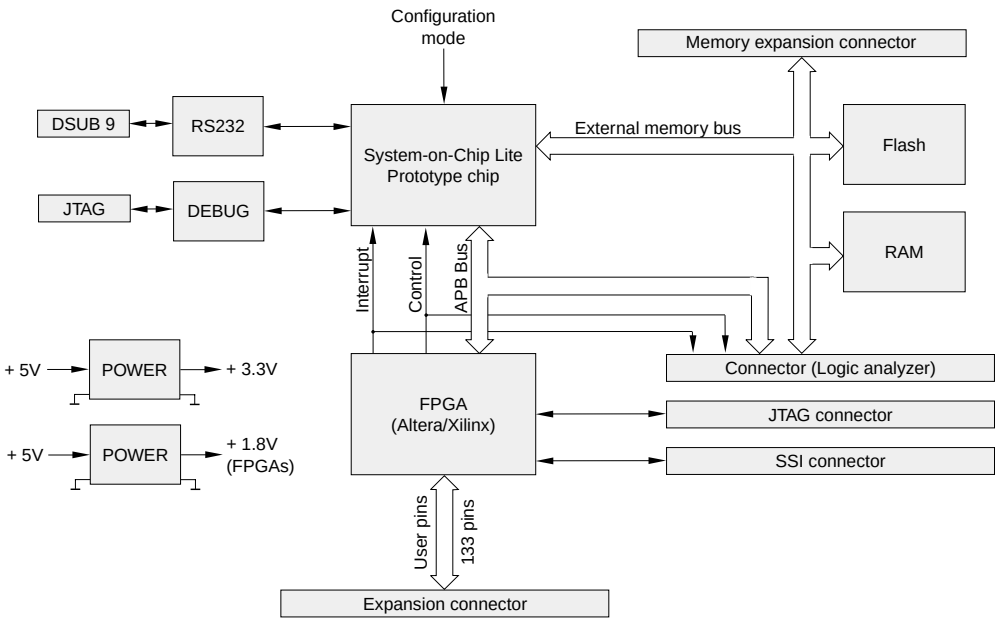
- FPGA: Xilinx or Altera
- Flash: 8Mbyte
- SRAM: 1Mbyte
- JTAG test and debug interface for the Multi-ICE
- RS232 interface with 9-pin Dsub connector
- Expansion connectors for Logic Analyser
- Expansion connectors for external memory bus and UDL pins
- Board Size: 220mm x 160mm
- Power supply included

Ordering Information

Part Number	FPGA vendor	FPGA size
EB-SoCLite-XI-2000E-6	Xilinx	2000k Gates
EB-SoCLite-AL-1000E-2x	Altera	1000k Gates

For further information on NEC products visit our European web-site at www.nec.de.

Block Diagram



System@IC
Solutions on a Chip

NEC

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Document No. A15046EE2V0PL00

With compliments